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Title: Challenges and Opportunities for Lattice QCD software

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We will consider recent trends in computer architecture, specifically the challenges posed by the increase in the number of cores/threads, longer vector units, complicated memory systems and the rise of accelerators, within the context of lattice QCD software. We will consider the impact these factors may have on lattice QCD codes and algorithms and discuss potential opportunities for performance portability on the road to the exascale.